Chip back potential is the level which bulk silicon is maintained by on-chip connection, or it is the level to which the chip back must be connected when specifically stated below. If no potential is given the chip back should be isolated.

**.054”**

**ANODE**

**ADJUST**

**CATHODE**

**.050”**

**Top Material: Al**

**Backside Material: Si**

**Bond Pad Size: .004 x .004”**

**Backside Potential:**

**Mask Ref:**

**APPROVED BY: DK DIE SIZE .050” X .054” DATE: 10/9/20**

**MFG: TEXAS INSTRUMENTS THICKNESS .025” P/N: LT1009**

**DG 10.1.2**

#### Rev B, 7/1